

Amendments to the Claims

1. (Cancelled).
2. (Cancelled).
3. (Cancelled).
4. (Cancelled).
5. (Cancelled).
6. (Cancelled).
7. (Cancelled).
8. (Cancelled).
9. (Cancelled).
10. (Cancelled).
11. (Currently Amended) A system for generating a pseudorandom delay, comprising:
 - a first linear feedback shift register for generating a pseudorandom number, the first linear feedback shift register comprises at least one tap and a variable seed value; and
 - a mapping system configured to assign each pseudorandom number value to a corresponding coarse delay value, the coarse delay value associated with an interval from a plurality of substantially equal intervals based on a range of the first linear feedback register and a number of coarse delay values[[.]]; and a pulse delay circuit

coupled to the mapping system for generating a first delay signal for controlling a signal receiver and for generating a second delay signal for controlling a signal transmitter.

12. (Original) The system of claim 11, further comprising a tap selector linear feedback shift register to pseudo-randomly set the at least one tap of the first linear feedback shift register.

13. (Original) The system of claim 11, further comprising a seed selector linear feedback shift register to pseudo-randomly set the seed value of the first linear feedback shift register.

14. (Original) The system of claim 11, further comprising a second linear feedback shift register configured to provide a fine delay value, the fine delay value including a fractional portion of the coarse delay value.

15. (Original) The system of claim 14, further comprising a control for selecting an output delay from the group consisting of the coarse delay value, the fine delay value, and a combination of the coarse delay value and the fine delay value.

16. (Original) The system of claim 11, the mapping system configured to assign each pseudorandom number value to a corresponding coarse delay value and a fine delay value, the fine delay value including a fractional portion of the coarse delay value.

17. (Cancelled).

18. (Cancelled).

19. (Cancelled).

20. (Cancelled).

21. (Currently Amended) A system for generating a randomized delay for a plurality of devices where each device has an associated delay, each device comprising:

a pseudorandom number generator for generating a sequence of pseudorandom numbers, each pseudorandom number of the sequence of pseudorandom numbers corresponding to a delay factor, the pseudorandom number generator comprising:

a pseudorandom number generator for generating the sequence of pseudorandom numbers;

a mapping system for mapping each of the pseudorandom numbers to a one of a discrete number of delay values; and

a system for pseudo-randomly generating at least one of a tap setting value and a seed value for each sequence of pseudorandom numbers[.]; and a pulse delay circuit coupled to the mapping system for generating a first delay signal for controlling a signal receiver and for generating a second delay signal for controlling a signal transmitter.

22. (Original) The system of claim 21, the pseudorandom number generator further comprising:

a first pseudorandom number generator for generating a coarse delay; and

a second pseudorandom number generator for generating a fine delay value, the fine delay value including a fractional portion of the coarse delay value.

23. (Cancelled).

24. (Cancelled).

25. (Cancelled).